

PROGRAMME FOR
DIPLOMA IN ELECTRONICS SEMESTER – VI
RE-EXAMINATION OCTOBER/NOVEMBER – 2014

THEORY

DAY/DATE	SUBJECT	SUB. CODE	TIME
	Interfacing Circuits	6001	01.30 p.m. to 03.30 p.m.
	Microcontrollers & Applications	6005	01.30 p.m. to 03.30 p.m.
	Power Electronics	6003	01.30 p.m. to 03.30 p.m.
	Instrumentation Engineering	6009	
	Principles of Control System Engineering	6007	01.30 p.m. to 03.30 p.m.

NAME OF SUPERINTENDENT OF EXAMINATION

P.V.POLYTECHNIC

Mrs. Shubhangini Joshi,
Principal In-Charge,
P. V. Polytechnic,
Juhu, Mumbai-400 049,

(Dr. P. N. Mandhare)
Controller of Examinations

NOTE: University reserves the right to make changes in the Examination schedule.

NT/-
25th July 2014